

# **Applications**

- 802.11n. MIMO solutions
- IEEE802.11b DSSS WLAN
- IEEE802.11g OFDM WLAN
- IEEE802.11a OFDM WLAN
- Access Points, PCMCIA, PC cards

#### **Features**

- 2 Transmit and 2 receive path architecture
- All RF ports matched to 50 Ω
- Integrated 2.4 GHz PA, 5 GHz PA, TX Filter, T/R switches and diplexers
- Integrated Power Detector for each TX Chain
- 18 dBm O/P Power, 802.11b, 11 Mbits, ACPR = 32 dBc
- 18 dBm @ 3.0 % EVM, 802.11g, 54 Mbits
- 15 dBm @ 3.0 % EVM, 802.11a, 54 Mbits
- Single supply voltage: 3.3 V ± 10 %
- Lead free and RoHS compliant
- Thin lead free plated package, 10 mm x 14 mm x 1.1 mm, MSL 3

# **Ordering Information**

Part No.	Package	Remark
SE2545A23	48 pin LGA	Samples
SE2545A23-R	48 pin LGA	Tape & Reel
SE2545A23-EK1	N/A	Evaluation kit

## **Product Description**

The SE2545A23 is a complete 802.11n WLAN RF front-end module providing all the functionality of the power amplifiers, power detector, T/R switch, diplexers and associated matching. The SE2545A23 provides a complete 2.4 GHz and 5 GHz WLAN Multiple Input, Multiple Output (MIMO) RF solution from the output of the transceiver to the antennas in an ultra compact form factor.

Designed for ease of use, all RF ports are matched to 50  $\Omega$  to simplify PCB layout and the interface to the transceiver RFIC. The SE2545A23 also includes a transmitter power detector for each band and transmit chain with 20 dB of dynamic range for each transmit chain. Each transmit chain has a separate digital enable control for transmitter power ramp on/off control. The power ramp rise/fall time is less than 0.5 µsec.

The device also provides a notch filter from 3.2-3.3 GHz and 3.2-3.9 GHz prior to the input of each 2.4 GHz and 5 GHz power amplifiers, respectively.

## **Functional Block Diagram**

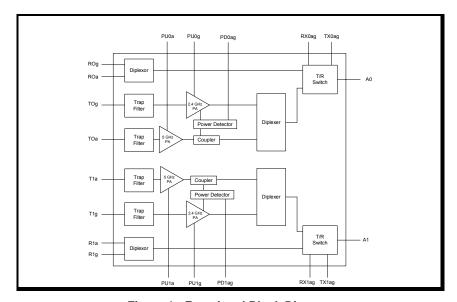


Figure 1: Functional Block Diagram



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#### **Product Preview**

The datasheet contains information from the product concept specification. SiGe Semiconductor, Inc. reserves the right to change information at any time without notification.

### **Preliminary Information**

The datasheet contains information from the design target specification. SiGe Semiconductor, Inc. reserves the right to change information at any time without notification.

Production testing may not include testing of all parameters.

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